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L3: Entry 1 of 2

File: JPAB

Aug 20, 1996

PUB-NO: JP408212140A

DOCUMENT-IDENTIFIER: JP 08212140 A

TITLE: MEMORY PROTECTION SYSTEM

PUBN-DATE: August 20, 1996

INVENTOR-INFORMATION:

NAME

COUNTRY

WADA, SHIGEAKI

SAIGA, TOSHIMASA

SEKINE, YASUO

MIYATA, HIDEFUMI

KUWABARA, KEIJI

INT-CL (IPC): G06 F 12/14

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC
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☐ 2. Document ID: JP 08212140 A

L3: Entry 2 of 2

File: DWPI

Aug 20, 1996

DERWENT-ACC-NO: 1996-429496

DERWENT-WEEK: 199643

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TITLE: Memory protection method for computer - involves using address conversion unit which consists of access provision unit that decides possibility of output of physical address to memory unit based on access propriety information

PRIORITY-DATA: 1995JP-0014925 (February 1, 1995)

PATENT-FAMILY:

PUB-NO

PUB-DATE

LANGUAGE

PAGES

MAIN-IPC

JP 08212140 A

August 20, 1996

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Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC
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